

Osptek Display

OLED SPECIFICATION

Model No:

ODM150-128128W001-P7

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General Specification

Item	Dimension	Unit
Dot Matrix	128 x 128 Dots	-
Module dimension	34.2 x 45.5 x 3.16	mm
Active Area	26.86 x 26.86	mm
Pixel Size	0.185 x 0.185	mm
Pixel Pitch	0.210 x 0.210	mm
Display Mode	Passive Matrix	
Display Color	White	
Drive Duty	1/128 Duty	
Frame rate	105Hz	
IC	SSD1327	
Interface	4w SPI	
Size	1.5 inch	



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Interface Pin Function

No.	Symbol	Function
1	GND	This is a ground pin.
2	VCC	Power supply for panel driving voltage. This is also the most positive power voltage supply pin. It is supplied by external high voltage source.
3	D0	These pins are bi-directional data bus connecting to the MCU data bus. Unused pins are recommended to tie LOW.
4	D1	When serial interface mode is selected, D0 will be the serial clock input: SCLK; D1 will be the serial data input: SID.
5	RES#	This pin is reset signal input When the pin is pulled LOW, initialization of the chip is executed. Keep this pin pull HIGH during normal operation.
6	D/C#	This pin is Data/Command control pin connecting to the MCU When the pin is pulled HIGH, the data at D[7:0] will be interpreted as data. When the pin is pulled LOW, the data at D[7:0] will be transferred to a command register.
7	CS#	This pin is the chip select input connecting to the MCU. The chip is enabled for MCU communication only when CS# is pulled LOW (active LOW).

Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply Voltage for Logic	VCC	1.65	5.5	V
Operating Temperature	TOP	-40	+80	°C
Storage Temperature	TSTG	-40	+85	°C

Electrical Characteristics

DC Electrical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage for Logic(3V/5V)	VCC	-	2.8	3.3	5.2	V
Input High Volt.	VIH	-	0.8xVCC	-	VCC	V
Input Low Volt.	VIL			-	0.2xVCC	V
Output High Volt.	VOH	-	0.9xVCC	-	VCC	V
Output Low Volt.	VOL	-	0	-	0.1xVCC	V
50% Check Board operating Current	ICC	VCC=3.3V	-	160	320	mA



Circuit Diagram

