

Osptek Display

OLED SPECIFICATION

Model No:

ODM130-12864W101-P4

osptek[®]

General Specification

Item	Dimension	Unit
Dot Matrix	128 x 64 Dots	-
Module dimension	35.5 x 33.5 x 2.6	mm
Active Area	29.42 x 14.7	mm
Pixel Size	0.21 x 0.21	mm
Pixel Pitch	0.23 x 0.23	mm
Display Mode	Passive Matrix	
Display Color	White	
Drive Duty	1/64 Duty	
Frame rate	105Hz	
IC	CH1116	
Interface	I2C	
Size	1.3 inch	



osptek®

Interface Pin Function

No.	Symbol	Function
1	GND	This is a ground pin.
2	VCC	Power supply for panel driving voltage. This is also the most positive power voltage supply pin. It is supplied by external high voltage source.
3	SCL	IIC Bus Clock Signal The transmission of information in the I2C bus is following a clock signal. Each transmission of data bit is taken place during a single clock period of this pin.
4	SDA	I2C Bus Data Signal This pin acts as a communication channel between the transmitter and the receiver.

Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply Voltage for Logic	VCC	1.2	6.0	V
Operating Temperature	TOP	-40	+70	°C
Storage Temperature	TSTG	-40	+85	°C

Electrical Characteristics

DC Electrical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage for Logic(3V/5V)	VCC	-	3.3	3.5	4.2	V
Input High Volt.	V _{IH}	-	0.8xVCC	-	VCC	V
Input Low Volt.	V _{IL}			-	0.2xVCC	V
Output High Volt.	V _{OH}	-	0.9xVCC	-	VCC	V
Output Low Volt.	V _{OL}	-	0	-	0.1xVCC	V
50% Check Board operating Current	I _{CC}	VCC=3.3V	-	180	300	mA



Circuit Diagram

